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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Anthony Richard Bonaccio, et al.
Serial No. : 09/682,473
Filed : September 6, 2001
For : CLOCK SIGNAL DISTRIBUTION UTILIZING
DIFFERENTIAL SINUSOIDAL SIGNAL PAIR
Examiner : An T. Luu
Group Art Unit : 2816

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Commissioner for Patents
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Alexandria, VA 22313-1450

INTERVIEW SUMMARY

On July 28, 2003, Applicants' undersigned attorney, Brian M. Dugan, called the Examiner to discuss the rejection of claims 1-42. During the telephone conversation, Applicants' undersigned attorney proposed amending all independent claims to state that the first and second sinusoidal signals are routed together. A further proposed amendment to claim 1 included routing the first and second sinusoidal signals together "to a

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Brian M. Dugan
(name of person certifying)

local clock generation location of the IC" and generating a clock signal "at the local clock generation location". The proposed amendments were submitted for discussion purposes only, although Applicants' attorney did not believe they were necessary for the allowance of claims 1-42. The proposed amendments were not entered.

The proposed amendments were discussed with reference to U.S. Patent Nos. 5,448,188 (Matsumoto et al.), 6,184,736 (Wissell et al.) and 5,157,227 (Tran et al.). Specifically, Applicants' attorney indicated that he did not believe such limitations were disclosed or taught by the above references; and also reiterated his belief that Matsumoto et al. has nothing to do with generating a clock signal.

The Examiner indicated that he did not believe that the above limitations would render the claims allowable over the above-cited references. The Examiner did indicate that including details regarding the receiver circuitry of Applicants' FIG. 2 in the pending claims might render the claims allowable. No agreement was reached.

Respectfully Submitted,



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AUG 27 2003

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Tarrytown, New York

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